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Dated: July 14, 2010

Electronic Signature for John J. Farrell: /John J. Farrell/

Docket No.: ROUND 3.0-131 DIV III
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Nathan R. Brown

Application No. 10/715,267

Filed: November 17, 2003

For: METHODS FOR POLISHING
SEMICONDUCTOR DEVICE STRUCTURES
BY DIFFERENTIALLY APPLYING
PRESSURE TO SUBSTRATES THAT
CARRY THE SEMICONDUCTOR DEVICE
STRUCTURES

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: Group Art Unit: 1716
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: Examiner: S. MacArthur
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action mailed April 16, 2010,
applicant submits the following amendments and remarks.